

Lessons Learned from MemVerge
Yue Li and Wei Kang
MemVerge
09/06/2019

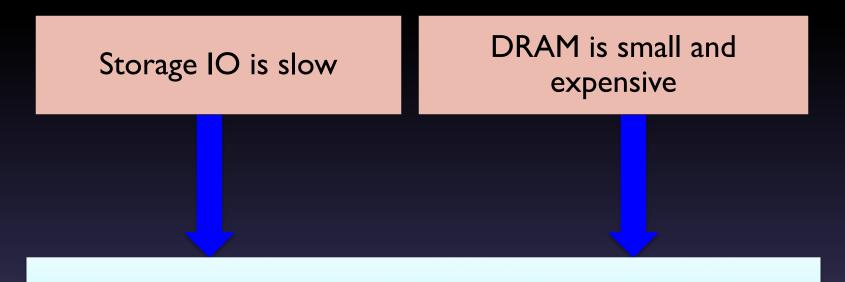
Agenda

- MemVerge Overview
- Our PMDK experience
 - libpmemobj
 - Memory allocation
 - Concurrent memory allocation
- Summary

About MemVerge

- Founded in 2017
- Headquartered in Milpitas, California, USA
- R&D teams in US and China
- Focus: next generation data infrastructure powered by persistent memory
 - Current product: MemVerge Distributed Memory Objects (DMO)
 - Beta version is now available for early PoC customers.
 - https://www.memverge.com/#beta

Data Infrastructure Pain Points



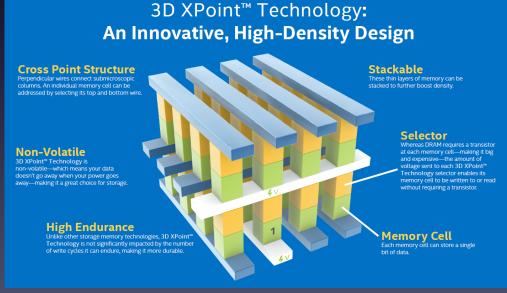
Could there be a solution that makes storage faster and memory bigger?

A New Class of Memory

- Persistent Memory (aka Storage Class Memory, NVRAM)
 - Low latency and high throughput, near the speed of DRAM
 - High density and non-volatility, like NAND flash
- Intel/Micron 3D XPoint
 - NVMe SSD (Optane SSD)
 - Byte-addressable DIMM (Optane DC PMEM)

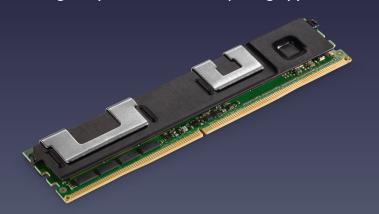






Storage Class Memory has Emerged!

- Intel Delivered Optane DC Persistent Memory based on 3D XPoint tech in Q2 2019
 - Revenue projected to reach \$3.6B by 2023
- Additional major vendors to join the foray by 2022
 - Making this a \$10B+ market by 2025
- Software ecosystem will be key for technology adoption
 - Identifying work load with strong ROI
 - Allowing adoption without requiring application rewrite







World's First Memory Converged Infrastructure

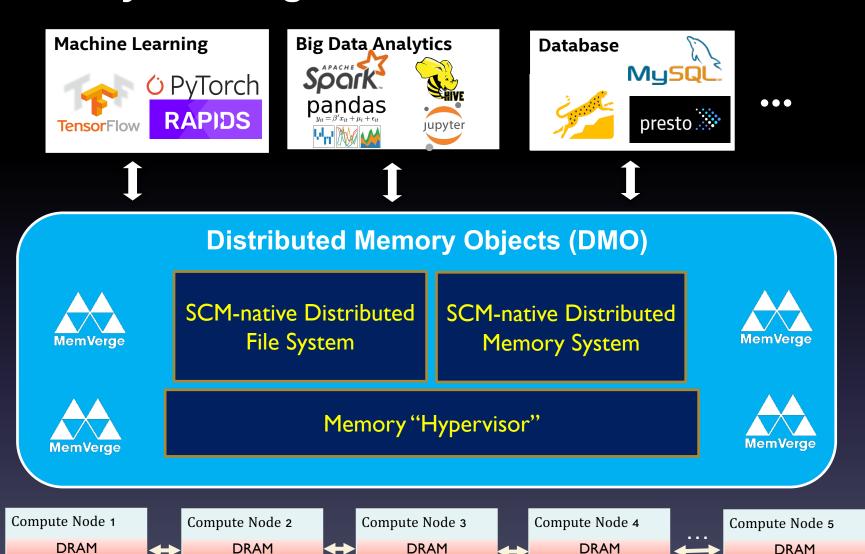
MemVerge software leverages Storage Class Memory technology to deliver larger memory and faster storage to applications without requiring application rewrites

SCM-native Distributed File System

SCM-native Distributed
Memory System

Memory "Hypervisor"

Memory Converged Infrastructure Platform



SCM

SSD

SCM

SSD

SCM

SSD

SCM

SSD

SCM

SSD

MemVerge is an Avid PMDK User

- This presentation
 - Share our stories as PMDK user
 - libpmemobj
 - Focus on memory allocation
 - Related Intel webinar on PMDK allocator

https://software.intel.com/en-us/videos/introduction-to-persistent-memory-allocator-and-transactions

Developing with PMEM

- Native support by recent Linux releases
 - e.g. CentOS/RHEL 7.5+ with vanilla kernel
- Great if real PMEM presents
 - e.g. Intel Optane DC PMEM
- PMEM emulation is easy
 - Augmenting grub with memmap=2G!4G
 - Works well on VMs, commodity laptops...
 - Emulated PMEM device survives across powered reboot

PMDK Strengths

- PMEM-native data access
 - pmem_persist and pmem_memcpy
 - Automatically applies platform-dependent optimization on accessing PMEM
 - Avoid your own tedious and error-prone implementations
- Transactions are readily available (for atomicity)
- Versatile
 - Different allocators (libpmemlog, libpmemblk, libpmemobj and libvmmaloc)
 - Different data abstractions (raw memory, object store, block, and key-value)
 - Support different drivers including pmem block device and device DAX

libpmemobj

- Allow objects get allocated and persisted in PMEM
- Simple to use
 - Create a pmem pool with pmempool utility, and we are ready to go
- Allocator has a good memory utilization across a wide range of allocation sizes
 - Great for I6K and 64K
 - Can be better for IMB+

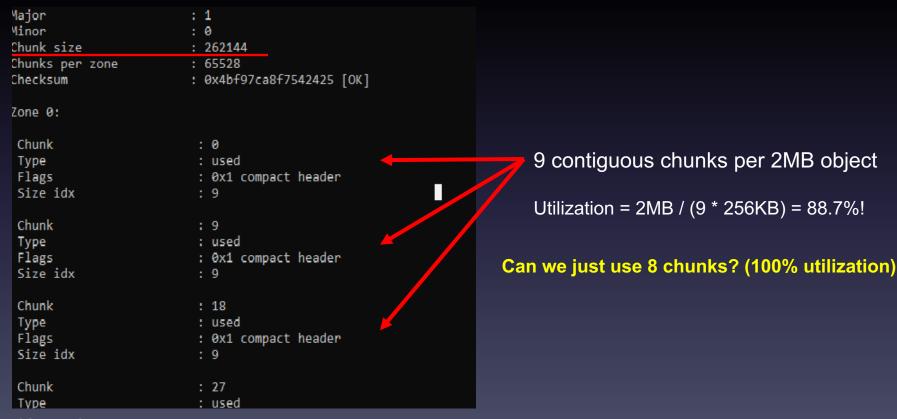
```
int main(int argc, char *argv[]) {
 PMEMobjpool *pop = pmemobj open("/dev/dax0.0", "mvdmo");
 if (pop == NULL || argc < 2) {</pre>
   return 1;
 uint64 t n = 0;
 uint32 t allocation size = std::stoul(argv[1]);
 for (;;) {
   int rc = pmemobj_alloc(pop, &oid, allocation_size, 1, nullptr, nullptr);
   if (rc)
     break;
   ++n;
 uint64 t totsize = 4225761280ULL;
 uint64 t utilized = n * allocation size:
 std::cout << totsize << "," << utilized << "," << totsize - utilized << " : "
     << utilized * 1000 / totsize << std::endl;
 pmemobj close(pop);
 return Θ;
```

Allocation Size	Utilization %
512	87.2
4096	92.1
16384	97.4
65536	98.2
262144	93.6
1048576	74.8
2097152	88.7

^{*}Assume using 4GB pool

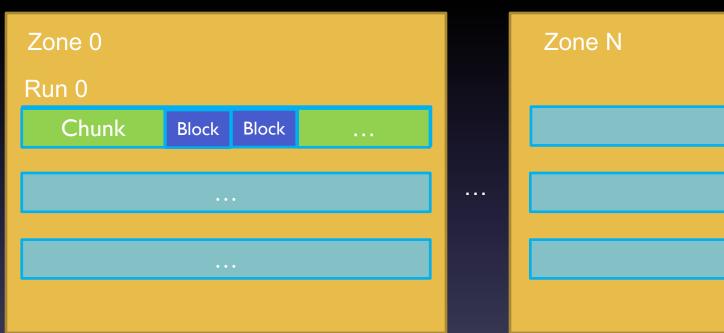
Understand the Results via pmempool info

Helps us understand our utilization results



^{*}Output for 2MB allocations

libpmemobj Persistent Heap Organization





pmempool info -H -C -b

Enables heap, chunk and bitmap information

```
Chunk size
                         : 262144
                         : 65528
Chunks per zone
Checksum
                         : 0x4bf97ca8f7542425 [OK]
Zone 0:
Chunk
                          : 0
Type
                          : run
                          : 0x9 compact header
Flags
Size idx
                          : 16
Block size
                          : 135296
Bitmap
                          : 24 / 31
XXXXXXXX XXXXXXXX XXXXXXXX .....
Chunk
                          : 16
Type
                          : run
Flags
                          : 0x9 compact header
Size idx
                          : 16
Block size
                          : 135296
                          : 24 / 31
Bitmap
XXXXXXXX XXXXXXXX XXXXXXXX ......
Chunk
                          : 32
Type
                          : run
Flags
                          : 0x9 compact header
Size idx
                          : 16
Block size
                          : 135296
Bitmap
                          : 24 / 31
XXXXXXXX XXXXXXXX XXXXXXXX ......
```

- I MB allocation
- 16 x 256KB contiguous chunks to create a "run" type area.
- Suballocated via bitmap.
- Only 3 x IMB allocations can be made from the 16 x 256KB area
 - which ideally could fit four.
 - Due to overhead
 - Explains 74.8 % utilization

How to Improve Utilization

- Custom allocation classes
 - Let you specify
 - Allocation unit size
 - Number of allocation units
 - Whether allocation unit has a header and what type
 - · The alignment of the units
 - libpmemobj has 53 pre-defined allocation classes
 - Allocator makes decision on which ones to use
 - Define additional allocation classes
 - Via pmemobj_ctl_set
 - Allocate through specific allocation classes
 - Function pmemobj_xalloc() can be parameterized by allocation class

Custom Allocation Class Example

• Allocation class has a name, following a naming convention

```
static void dump_alloc_classes (PMEMobjpool *pop) {
  char cls_desc[32];
  struct pobj_alloc_class_desc cls;
  for (int i = 0; i < 255; i++) {
    snprintf(cls_desc, sizeof(cls_desc), "heap.alloc_class.%d.desc", i);
    if (pmemobj_ctl_get(pop, cls_desc, &cls) >= 0) {
        printf("%s %lu\n", cls_desc, cls.alignment);
        printf("cls.unit_size: %lu\n", cls.unit_size);
        printf("cls.units_per_block: %d\n", cls.units_per_block);
        printf("cls.class_id: %d\n", cls.class_id);
    }
}
```

pmemobj_ctl_get can retrieve the allocation class.

```
heap.alloc class.0.desc 0
cls.unit size: 262144
cls.units per block: 0
cls class id: 0
heap.alloc class.1.desc 0
cls.unit size: 128
cls.units per block: 2045
cls.class id: 1
heap.alloc class.2.desc 0
cls.unit size: 192
cls.units per block: 1364
cls.class id: 2
heap.alloc class.51.desc 0
cls.unit size: 246720
cls.units per block: 17
cls.class id: 51
heap.alloc class.52.desc 0
cls.unit size: 262080
cls.units per block: 16
cls.class id: 52
heap.alloc class.53.desc 0
cls.unit size: 349440
cls.units per block: 12
cls.class id: 53
```

Custom Allocation Class to Improve Utilization

- Class 54 is the first unused class
- Let's use it for an efficient IMB allocation
- 4KB alignment (page aligned)
- No header
- 256 units per run

- Use pmemobj xalloc()
 - with POBJ_CLASS_ID() option

```
tatic int custom_alloc_class(PMEMOD)pool *pop, size_t unit_size) {
 pobi alloc class desc cls:
 cls.alignment = 4096:
 cls.header type = POBJ HEADER NONE;
 cls.unit size = unit size;
 cls.units per block = 256;
 cls.class id = 54; /* the first class id available after the pre-defined classes */
 if (pmemobj_ctl_set(pop, "heap.alloc_class.new.desc", &cls) < 0) {</pre>
   printf("Failed to create allocate class for unit size: %lu\n", cls.unit size);
   return -1:
 return cls.class id;
.nt main(int argc, char *argv[]) {
 PMEMobjpool *pop = pmemobj open("/dev/dax0.0", "mvdmo");
if (pop == NULL) {
   return 1:
 uint32_t allocation_size = 1024 * 1024;
 int clsid = custom alloc class(pop, allocation size);
 if (clsid < 0) {
   pmemobi close(pop):
   return -1;
 uint64 t n = 0;
 for (;;) {
   PMEMoid oid:
   int rc = pmemobj xalloc(pop, &oid, allocation size, 1, POBJ CLASS ID(54), nullptr, nullptr);
   if (rc)
     break;
   ++n;
uint64 t totsize = 4225761280ULL;
 uint64 t utilized = n * allocation size;
 std::cout << totsize << "," << utilized << "," << totsize - utilized << " : "
     << utilized * 1000 / totsize << std::endl;
 pmemobj close(pop);
 return A.
```

Utilization becomes 95% for IMB allocation

Before: using default allocation class

Chunk size : 262144 Chunks per zone : 65528

Checksum : 0x4bf97ca8f7542425 [OK]

Zone 0:

Chunk : 0 Type : run

Flags : 0x9 compact header

Size idx : 16
Block size : 135296
Bitmap : 24 / 31
xxxxxxxx xxxxxxx xxxxxxx

Chunk : 16 Type : run

Flags : 0x9 compact header

Size idx : 16 Block size : 135296 Bitmap : 24 / 31 XXXXXXXXX XXXXXXXX

Chunk : 32 Type : run

Flags : 0x9 compact header
Size idx : 16
Block size : 135296
Bitmap : 24 / 31
xxxxxxxx xxxxxxx xxxxxxx

After: using custom allocation class

Chunk size : 262144 Chunks per zone : 65528

Checksum : 0x4bf97ca8f7542425 [0K]

Zone 0:

Chunk : 0 Type : run

Flags : Oxe header none

 Size idx
 : 1025

 Block size
 : 1048576

 Bitmap
 : 255 / 255

Chunk : 1025
Type : run
Flags : 0xe h

Flags : 0xe header none Size idx : 1025 Block size : 1048576

Block size : 1048576 Bitmap : 255 / 255

Tips for Custom Allocation Class

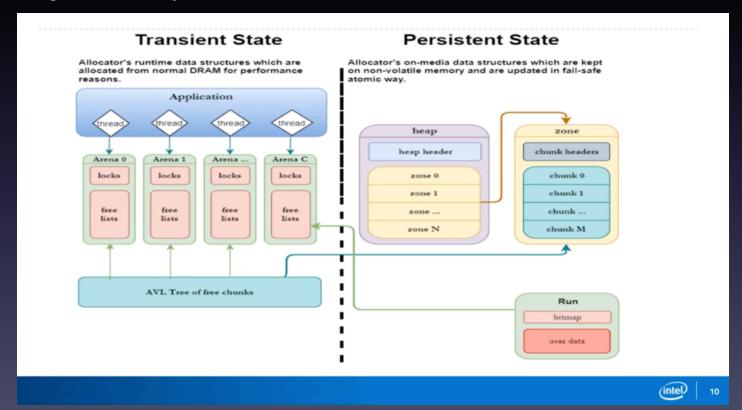
- Dramatically improves pool utilization
 - Especially when allocating objects with sizes chosen from a fixed small set
 - e.g. {4KB, 8KB, ..., IMB}
- Finding best custom parameters can still be challenging
 - Needs experiments
- Use pmempool to sanity check the results
- Improper use of allocation classes results in very bad utilizations
 - E.g. allocate very small objects using our custom class 54...

Failures of Parallel Allocation

- Allocation failures can occur even though ample space remains in PMEM pool
 - Multi-threading
 - Variant object size
 - Customized allocation classes

Allocation Arena

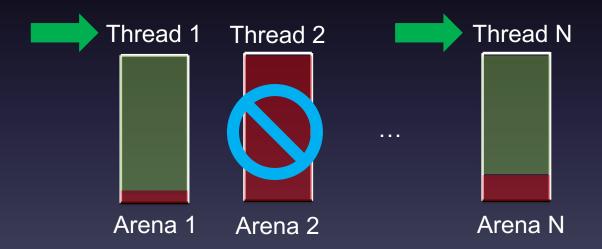
- libpmemobj uses arena to provide support for multithreaded allocations
 - Each thread is assigned a separate arena
 - High concurrency



A Hypothesis

Unbalanced use of arenas

- Arena reserve memory per allocation class
- Thread is assigned to an arena at first allocation



To Validate Our Hypothesis

- Two sets of threads
 - Set I: allocate objects in parallel

May fail due to unbalanced allocation

Set 2: free objects allocated by Set 1 in parallel

```
int free_objects(PMEMobjpool *pool, PMEMoid *objects, unsigned object_count) {
  unsigned i = 0;
  int rc = 0;
  TX_BEGIN(pool) {
    for (; i < object_count; i++) {
       if (OID_IS_NULL(objects[i])) {
          continue;
       }
       pmemobj_tx_free(objects[i]);
    }
} TX_ONABORT {
    rc = -pmemobj_tx_errno();
    LOGERR("Free object %u failed: %d", i, rc);
} TX_END;
return rc;
}</pre>
```

May also fail!



Transactional deallocation also requires <u>allocating</u> log in PMEM!

Solutions in PMDK 1.5

- Retry in user application via a different thread
- Arena pre-reservation

Solutions in PMDK 1.6

PMDK 1.6 allows a thread to manually change arena

```
- pmemobj_ctl_get("heap.arena.[arena_id].size")
```

- pmemobj_ctl_set("heap.thread.arena_id")

Multiple solutions can be developed based on this facility

Solutions

- Use single arena
 - Easy to implement
 - Hurt concurrency due to single lock contention
- Allocation class based arena
 - Simple to implement
 - Minor conflicts on arena (when you have many threads allocating same sized objects)

Solutions cont.

Choose a different arena upon allocation failure



Evaluation

Test bed

- CPU: Intel(R) Xeon(R) Gold 6252 CPU @ 2.10GHz 96 cores
- Dax: I AEP Dimm 128G

Methodology

- 4 threads allocate objects in parallel
- 8 threads free objects in parallel
- Object size: 4KB, 8KB, ..., 64MB, one allocation class per object size
- Stop after total allocated 4TB objects in total
- Metric: the average time to stop the execution

Results

Solution	Time
Single arena	46.092s
Use arena for every allocation class	41.072s
Retry allocation in other arena	33.078s

^{*} Allocations are fully randomized, results may change for other allocation patterns

Summary

- PMEM poses new challenges to software and hardware
 - High performance, high memory utilization, less fragmentation...
- SPDK, DPDK, PMDK are extremely useful as components in a distributed system.
- PMDK has ways to increase performance and productivity for PMEM applications
 - but requires advanced programming techniques and developer attention.
 - memory allocation is only a small tip of the iceberg
- MemVerge and Intel together deliver better scalability and performance at a lower cost via a disruptive PMEM-optimized data infrastructure
 - Al, Big Data, Banking, Animation Studios, Gaming Industry, IoT, etc.
 - Machine learning, Analytics, and Online systems

- We are in Beta! To become our PoC customer
 - https://www.memverge.com/#beta

- Join us! We are hiring strong engineering candidates
 - Locations: Shanghai, Beijing, Silicon Valley
 - https://www.memverge.com/careers/



Check out our demo!