

OPTIMIZE SYSTEM CONFIGURATIONS AND WORKLOADS FOR INTEL® OPTANE™ DC PERSISTENT MEMORY

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3-STEPS FOR OPTIMIZING COMPLEX WORKLOADS



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THE LONG & SHORT OF PERFORMANCE ANALYSIS

GET THE BIG PICTURE FIRST WITH A SNAPSHOT OR PLATFORM PROFILER

	Snapshot Quickly size potential performance gain. Run a test "during a coffee break".	In-Depth Advanced collection & analysis. Insight for effective optimization.		
Application Focus • HPC App developer focus • 1 app running during test	VTune Amplifier's Application Performance Snapshot L®	VTune Amplifier • Many profiles S-MIntel Advisor • VectorizationSITAC • MPI OptimizationS-L		
System FocusDeployed system focusFull system load test	VTune Amplifier's Storage Performance Snapshot	VTune Amplifier - System-wide sampling - Platform Profiler LP		

Maximum collection times: L[®]=long (hours) M[®]=medium (minutes) S[®]=short (seconds-few minutes)

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INTEL[®] VTUNE[™] AMPLIFIER - PLATFORM PROFILER

Targets infrastructure and software architects

Longer runs - system-wide data

Interactive Topology Diagrams

- System configuration
- Memory channel configurations



Performance metrics

- Low overhead (targeting < 1%) coarse grain</p>
- Sampling OS and HW performance counters
- Extended capture (min. to hours)
- Open Data model with RESTful API for easy analysis by scripts



Core to Core Comparisons

uOPS Delivered (average/core)

Memory Ops Per Instruction (average/core)



Loads Stores

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INTEL[®] VTUNE[™] AMPLIFIER Platform profiler

A guided tour



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See Platform Profiler in Action

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Matrix Multiplication

This sample was collected using an application derived from the Intel® Math Kernel Library Matrix Multiplication C Sample. The sample is not heavily CPU-bound and demonstrates efficient memory usage and throughput. However, the application is only running on a few cores and could benefit from parallelization. The sample was collected on an Ubuntu* Linux system with 64GB of DRAM and 36 cores.

File Copy

This sample was collected while continuously copying 30+ randomly sized files for five minutes on socket 1 followed by five minutes on socket 0. It shows a marked increase in efficiency when using socket 0 and demonstrates that applications that use a lot of file operations can benefit from using the socket closest to storage. The sample was collected on an Ubuntu* Linux system with two sockets and one NVMe* disk.

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Memory Sample

This sample monitors a memory traversal program that walks a 32GB allocation in varying steps. The same program is run five times using different core combinations and has a 10 second delay between each run. The sample was collected on an Ubuntu* Linux system with 64GB of DRAM and 200GB swap space.

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COLLECTING DATA



Linux*

Works best with Chrome

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IDENTIFYING OPPORTUNITIES

Characterizing workloads for Intel[®] Optane[™] DC Persistent Memory

THE BEST OF BOTH WORLDS WITH INTEL[®] OPTANE[™] DC PERSISTENT MEMORY

DRAM ATTRIBUTES

Performance comparable to DRAM at *low latencies*¹



NAND SSD ATTRIBUTES

Data persistence with higher capacity than DRAM²

 "Fast performance comparable to DRAM" - Intel persistent memory is expected to perform at latencies near DDR4 DRAM. Benchmarks and proof points forthcoming. "I/ow latencies" - Data transferred across the memory bus causes latencies to be orders of magnitude lower when compared to transferring data across PCIe or I/O bus' to NAND/Hard Disk. Benchmarks and proof points forthcoming.

 Intel persistent memory offers 3 different capacities – 128GB, 256GB, 512GB. Individual DIMMs of DDR4 DRAM max out at 256GB.

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Optane[™]

Media



PROGRAMMING MODELS



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WHAT TO LOOK FOR

Memory and storage behavior

- Memory utilization
- Page faults
- Memory read/write ratios
- Memory throughput
- Disk activity



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WHAT TO LOOK FOR

Performance impact

- CPU utilization (user/kernel)
- Cycles per instructions (CPI)
- Non-uniform memory architecture (NUMA) and inter-socket traffic



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UNDERSTANDING CONFIGURATIONS AND PERFORMANCE

Finding the right system for your workloads

CONFIGURATION MATTERS!



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WHICH CONFIGURATION IS BEST FOR YOUR APP?

Tradeoff: DRAM / DCPMM bandwidth and cost

2-2-2

12 slots per CPU Max memory capacity and bandwidth





board real estate on the DIMM slots

1-1-1



6 slots per CPU Least number of DIMM slots to utilize max memory bandwidth

* No difference on functionality or performance when 2nd DIMM slot is in channel 0, 1 or 2 for that integrated memory controller (IMC)
† DIMM slots shown. While DRAM DIMMs can populate all slots shown, DCPMM is only populated in slot closest to CPU in each channel.

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MEMORY UTILIZATION



Memory Utilization





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MEMORY THROUGHPUT



Socket 0 - Persistent Memory Traffic (per Channel)







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UNDERSTANDING CONFIGURATIONS AND PERFORMANCE

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PERFORMANCE METRICS

Memory throughput

- Read/write throughput
- Read/write ratio
- Persistent vs. volatile

Memory latency

- DRAM read/write latencies
- Persistent memory read/write latencies

Memory Mode

Near memory cache miss rate



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SUMMARY

What's next



Use Intel[®] VTune[™] Amplifier – Platform Profiler

- To get useful insights into system behavior and performance
- To identify opportunities and optimize for Intel[®] Optane[™] DC Persistent Memory

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DOWNLOAD THE TOOLS

Software tools for Intel[®] Optane[™] DC Persistent Memory Free downloads and technical articles

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